



TAPAS community challenge - powered by Siemens

- → Open for everyone (including students, employees, makers in general) interested in software defined inverters, power electronic, programming, ...
- → Includes a **FREE Tapas board** from Siemens to realize the projects
- → Runs until 13.06.2018, finalists presentation and award ceremony (including <u>5000€ cash price</u>) at FAU Erlangen on 27.07.18

Participants will have the opportunity to share their ideas with Siemens' digital experts and receive a free TAPAS board. As a finalist they present the project to a top industrial and academic audience. The winner team will get a 5.000 Euro cash prize.

How to participate

- Register at https://challenge.tapas.sdi.tools
- Post and discuss your idea and get your TAPAS board for free
- Realize your project, convince with your results until 13.06.18 and win a spot in the group of finalists

The technology

Software Defined Inverters (SDIs) are the future of power converters. They achieve universality with a single, fixed piece of hardware where the overall functionality is defined via software (changes) only. The GaN (Gallium Nitride) technology behind TAPAS allows for a high fidelity smooth output signal.

Facts about the TAPAS Board

- ~300W @48V GaN
- Light 80 gram (200g incl. housing)
- No heat sink / PCB convection cooling
- High bandwidth (>300kHz switching frequency)
- Smooth output (on-board filter)
- Universal can be used for robotics, motor drives, battery charging, DC/DC, AC/DC, audio, etc.
- Open source Raspberry PI compatible

More information

On the Challenge

- Link to the Challenge: https://challenge.tapas.sdi.tools/
- Link to Video on LinkedIN
 <u>https://www.linkedin.com/feed/update/urn:li:activity:6376826518039076864/</u>
- Link to Video on Twitter https://twitter.com/BuschRo/status/971060665019523072

On the TAPAS Board itself

- Pictures of the Future Article: <u>https://www.siemens.com/innovation/en/home/pictures-of-the-future/industry-and-automation/the-future-of-manufacturing-innovative-inverter.html</u>
- GitHub Link https://github.com/SDI-SoftwareDefinedInverter/TAPAS/blob/master/README.md

Contact

Dipl.-Ing. Christine Schichler Siemens CKI Managerin Forschungs- & Technologie-Haus Mandellstrasse 9/II, 8010 Graz christine.schichler@tugraz.at phone +43 (0) 316 873 6039 mobil +43 664 60 873 6039

