

WORLD



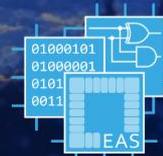
RISC-V[®]

DAYS



Reconfigurable Computer Architectures
Embedded Architectures & Systems Group
Institute of Technical Informatics
Graz University of Technology

#RISCVEverywhere



***“to bring the RISC-V community
together globally to share in a
synchronized event - worldwide”***

– mission statement, RISC-V International





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Pitch Talks



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› Maja Malenko, CHERI Ambassador



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- › Leo Moser, Open Silicon Advocate



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- › Tiberio Fanti, NXP Semiconductors
- › Olivier Léchevin, AT-C³ / Silicon Alps Cluster



Pitch Talks

→ Poster / Showcases
& Networking



Pitch Talk



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