Master's thesis

In cooperation with logicdev e.U. logicdev

Developing an external calibration method to compensate the temperature effect on OPAMP.

Current Status and Motivation:

Present choice = tomorrow future. logicdev is working towards the Futuristic energy product for Industry. At logicdev, we Enjoy the curiosity to create the best.

Research Topic(s):

Investigating the Temperature effect on Operational Amplifiers and developing an external calibration method.

Most of the available techniques can minimize VIO at only defined temperature. It is hard to predict or correct the output change or error due to change in temperature.

Is there a way for a continuous correction/calibration, over a temperature range, so they have very low drift (Vio & Vout). Your solution must be applicable for all manufacturing process technology (Bipolar, BiFET, CMOS, GAN). example: Close loop with temperature sensitive transistor should be used in compensation network. We are 996 there to support you to solve this problem.

Approach / Methodology:

- Supervise by IFE, TU Graz
- Direct contact with companies' engineers and experts
- Literature- and internet database
- Online training & webinar

Organisational Matters:

- Start of work: June 2022
- Workplace: IFE/TU Graz
- Paid thesis: (salary)

Contact person / Supervisor:

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